

CLAIM AMENDMENTS

1. (Currently Amended) ~~A high-frequency~~ A semiconductor integrated circuit switching between respective electrical characteristics of the semiconductor integrated circuit, comprising:

~~a main circuit consisting of~~ having an active element; and a first pad; and at least one transmission line;

a circuit block including a passive element; and

a second pad connected to said circuit block; and a wire connecting wherein said first pad is selectively connected to and disconnected from said second pad by a wire.

2. (Currently Amended) ~~The high-frequency~~ A semiconductor integrated circuit according to claim 1 switching between a plurality of characteristics of the semiconductor integrated circuit, comprising:

a main circuit having an active element and a first pad;

a circuit block including a passive element; and

a second pad connected to said circuit block, wherein

said first pad is selectively connected to and disconnected from said second pad by a wire,

said main circuit includes an input terminal and an output terminal and said active element and said first pad are located between said input terminal and said output terminal,

said circuit block includes an interconnect connected to said second pad, and

said wire and said interconnect have lengths totaling one-quarter of one wavelength of a signal input to said input terminal.

3. (Currently Amended) ~~The high-frequency according to claim 2~~ A semiconductor integrated circuit switching between a plurality of characteristics of the semiconductor integrated circuit, comprising:

a main circuit having an active element and a first pad;

a circuit block including a passive element; and

a second pad connected to said circuit block, wherein said passive element has an impedance that decreases with an increase in frequency of said first pad is selectively connected to and disconnected from said second pad by a wire, and when said first pad is

connected to said second pad by the wire, said main circuit transmits an input signal input to, preventing transmission leakage of said input-terminal signal.

4. (Currently Amended) A ~~high frequency~~ semiconductor integrated circuit comprising:
a main circuit having an active element and a first pad;
a circuit block including a passive element;
a second pad connected to said circuit block; and
a wire connecting said first pad to said second pad, wherein
said main circuit includes an input terminal and an output terminal and said active element and said first pad are located between said input terminal and said output terminal,
said circuit block includes an interconnect connected to said second pad,
and
said wire and said interconnect have lengths totaling one-fourth of a wavelength of a ~~high frequency~~ signal input to said input terminal.

Claims 5-15 (Cancelled).